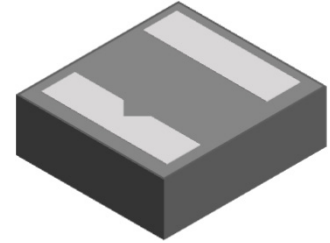
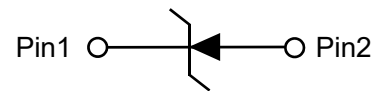
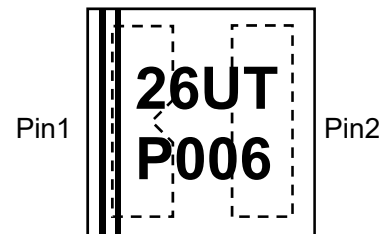


**Description**

The PTVSHC2QN26VUT Transient Voltage Suppressor is designed to replace multilayer varistors (MLVs) in portable applications such as cell phones, notebook computers, and PDA's. They feature large cross-sectional area junctions for conducting high transient currents, offer desirable electrical characteristics for board level protection, such as fast response time, lower operating voltage, lower clamping voltage and no device degradation when compared to MLVs. The PTVSHC2QN26VUT protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. The PTVSHC2QN26VUT is available in a DFN1616-2L package with working voltages of 26 volt.


**DFN1616-2L(Bottom View)**

**Circuit Diagram**
**Feature**

- 5600W peak pulse power per line ( $t_p = 8/20\mu s$ )
- DFN1616-2L package
- Protect one I/O or power line
- Low clamping voltage
- RoHS compliant
- Transient protection for data lines to
  - IEC 61000-4-2(ESD)  $\pm 30kV$ (air),  $\pm 30kV$ (contact);
  - IEC 61000-4-4 (EFT) 40A (5/50ns);
  - IEC 61000-4-5 (Lightning) 160A (8/20us)


**Marking (Top View)**
**Applications**

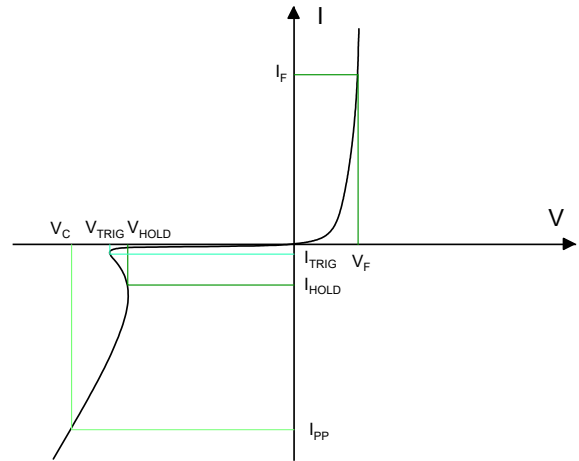
- Cell phone handsets and accessories
- Personal digital assistants (PDA's)
- Notebooks, desktops, and servers
- Portable instrumentation
- Cordless phones
- Digital cameras
- Peripherals
- MP4 players

**Mechanical Characteristics**

- Lead finish: 100% matte Sn(Tin)
- Mounting position: Any
- Qualified max reflow temperature: 260°C
- Pure tin plating: 7 ~ 17 um
- Pin flatness:  $\leq 3mil$

## Electronics Parameter

Symbol	Parameter
$V_{RWM}$	Peak Reverse Working Voltage
$I_R$	Reverse Leakage Current @ $V_{RWM}$
$V_{TRIG}$	Reverse trigger Current
$V_{HOLD}$	Reverse holding voltage
$I_T$	Test Current
$I_{PP}$	Maximum Reverse Peak Pulse Current
$V_C$	Clamping Voltage @ $I_{PP}$
$P_{PP}$	Peak Pulse Power
$C_J$	Junction Capacitance
$I_F$	Forward Current
$V_F$	Forward Voltage @ $I_F$



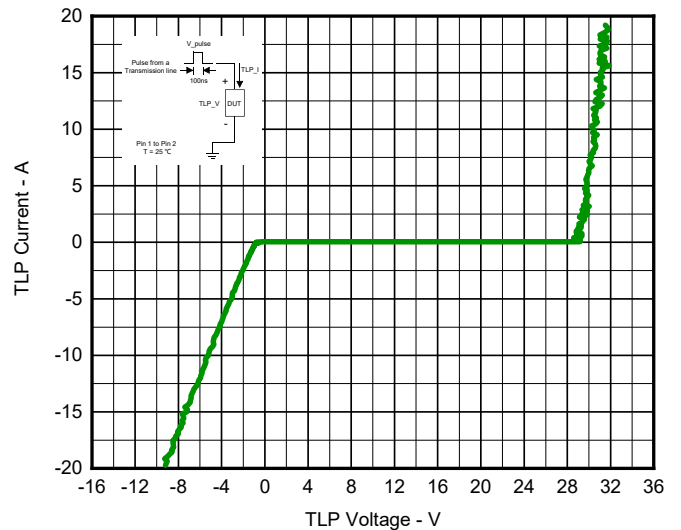
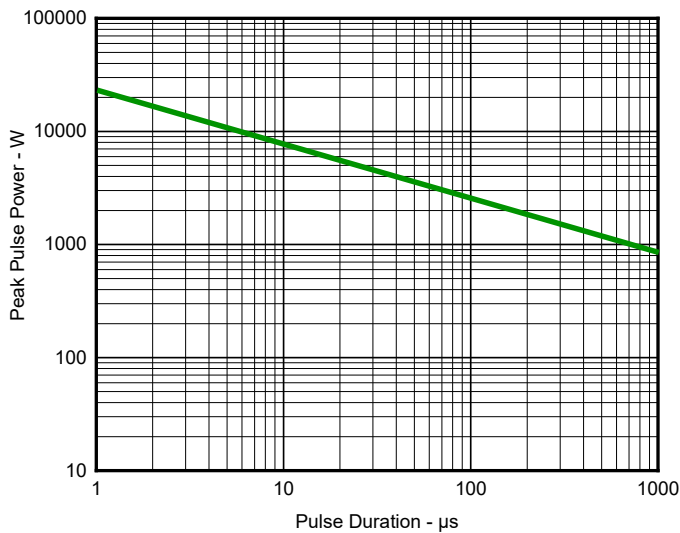
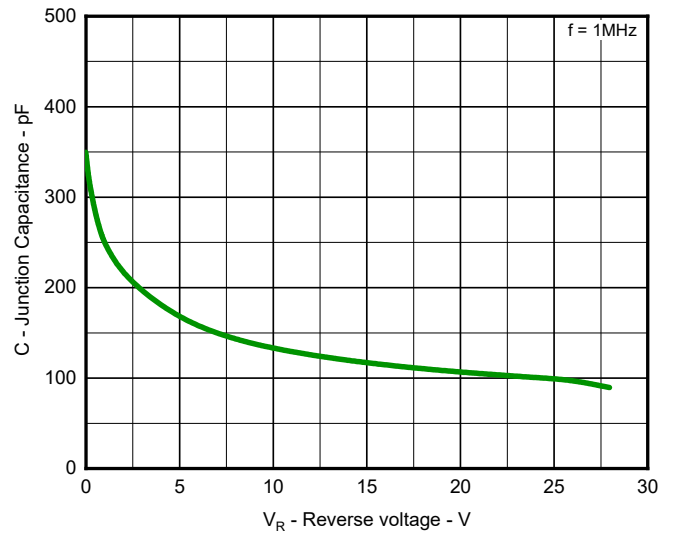
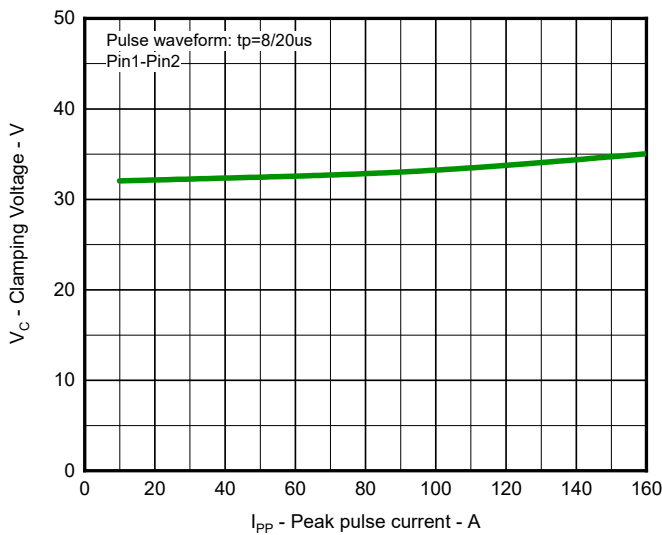
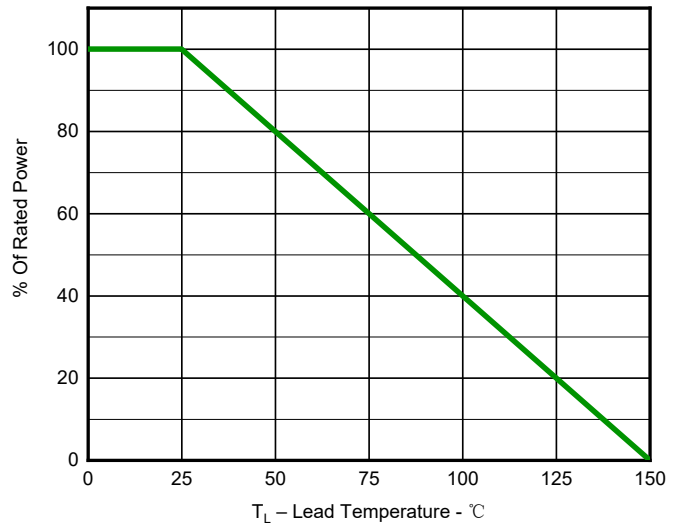
## Electrical characteristics per line@25°C (unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Peak Reverse Working Voltage	$V_{RWM}$	-	-	-	26	V
Breakdown Voltage	$V_{BR}$	$I_t = 1\text{mA}$	27	-	31	V
Reverse Leakage Current	$I_R$	$V_{RWM} = 26\text{V}$	-	-	1	$\mu\text{A}$
Forward voltage	$V_F$	$I_F = 10\text{mA}$	0.5	-	1.0	V
Clamping Voltage	$V_C$	$I_{PP} = 160\text{A}, t_p = 8/20\mu\text{s}$	-	35	38	V
Junction Capacitance	$C_J$	$V_R = 0\text{V}, f = 1\text{MHz}$	-	330	500	pF

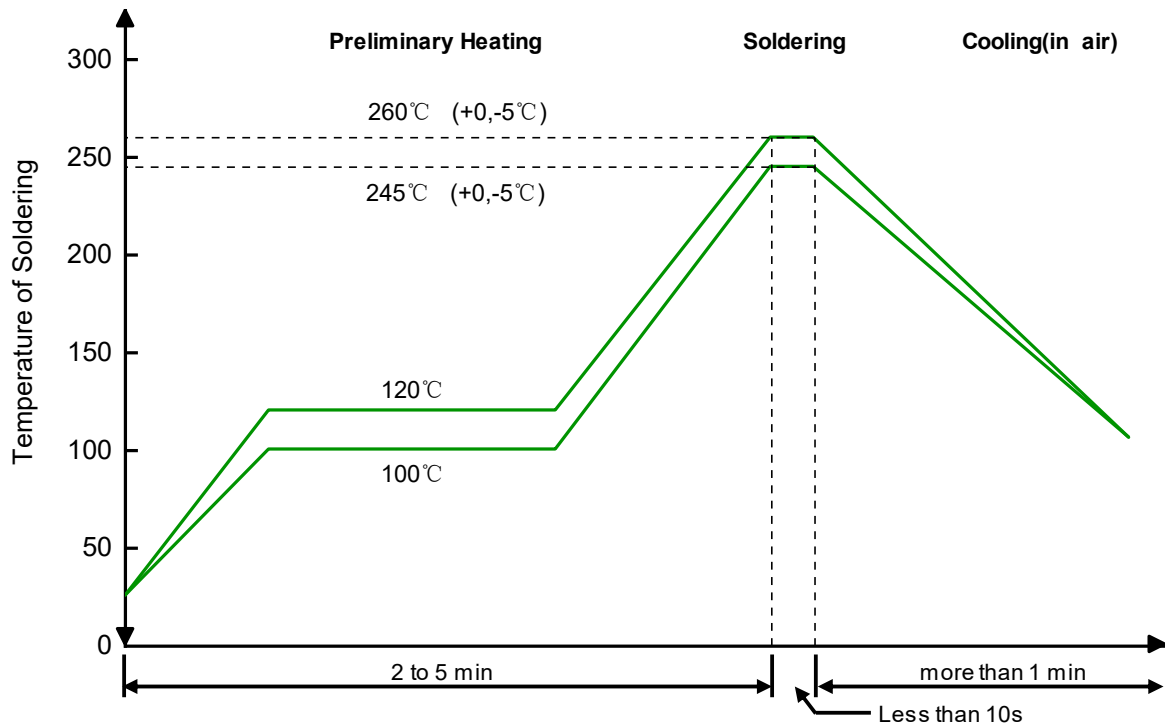
## Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Peak Pulse Power ( $t_p = 8/20\mu\text{s}$ )	$P_{PP}$	5600	W
Peak Pulse Current ( $t_p = 8/20\mu\text{s}$ )	$I_{PP}$	160	A
Lead Soldering Temperature	$T_L$	260 (10 sec)	°C
Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~+150	°C
ESD Protection-Contact Discharge	$V_{ESD}$	$\pm 30$	kV
ESD Protection-Air Discharge	$V_{ESD}$	$\pm 30$	kV

## Typical Characteristics



## Solder Reflow Recommendation



Remark: Pb free for 260°C; Pb for 245°C.

## PCB Design

For TVS diodes a low-ohmic and low-inductive path to chassis earth is absolutely mandatory in order to achieve good ESD protection. Novices in the area of ESD protection should take following suggestions to heart:

- Do not use stubs, but place the cathode of the TVS diode directly on the signal trace.
- Do not make false economies and save copper for the ground connection.
- Place via holes to ground as close as possible to the anode of the TVS diode.
- Use as many via holes as possible for the ground connection.
- Keep the length of via holes in mind! The longer the more inductance they will have.

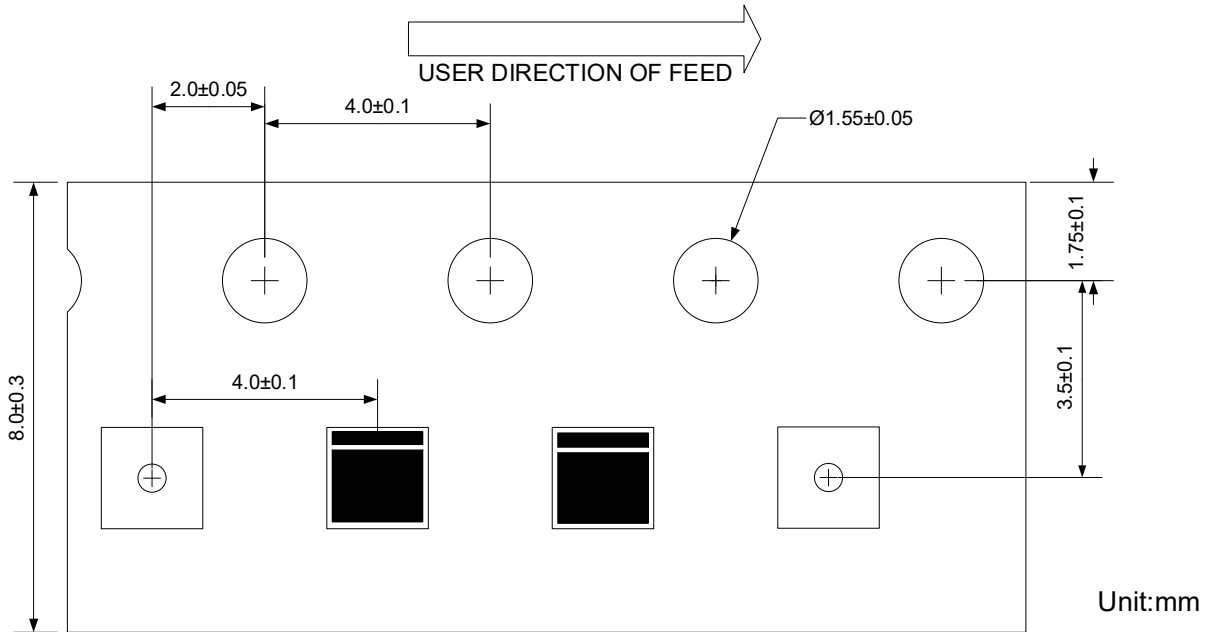
## Ordering information

Device	Package	Reel	Shipping
PTVSHC2QN26VUT	DFN1616-2L (Pb-Free)	7"	3000 / Tape & Reel

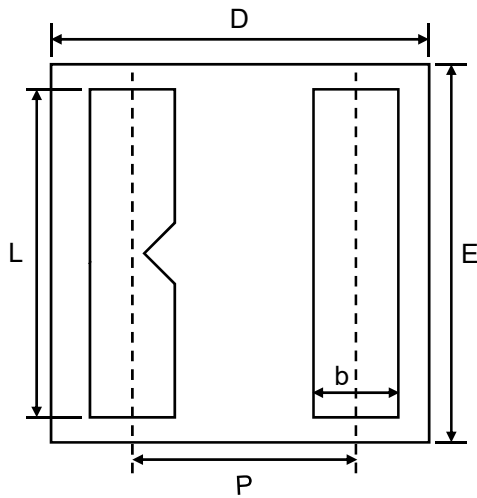
# Transient Voltage Suppressor

# PTVSHC2QN26VUT

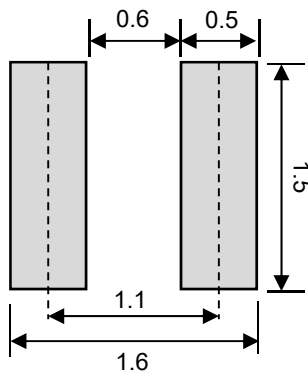
## Load with information



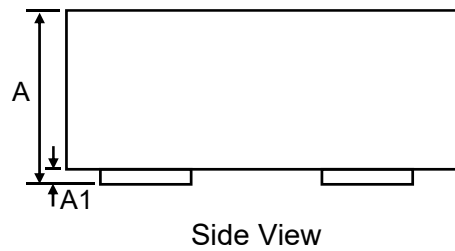
## Product dimension (DFN1616-2L)



Bottom View




Suggested PCB Layout



Side View

Dim	Millimeters		Inches	
	Min	Max	Min	Max
A	0.50	0.577	0.020	0.023
A1	0.00	0.05	0.000	0.002
b	0.25	0.35	0.010	0.014
D	1.50	1.70	0.059	0.067
E	1.50	1.70	0.059	0.067
L	1.25	1.35	0.049	0.053
P	1.10 BSC		0.043 BSC	


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