

## PSBD3TD45V10H

# Schottky Barrier Diode

#### **Feature**

- High Forward Surge Capability
- Ultra Low Forward Voltage
- Excellent High Temperature Stability

## **Applications**

 For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

## Construction

Silicon epitaxial planar

#### **Mechanical Characteristics**

- Lead finish:100% matte Sn(Tin)
- Mounting position: Any
- ➤ Qualified max reflow temperature:260°C
- ➤ Pure tin plating: 7 ~ 17 um

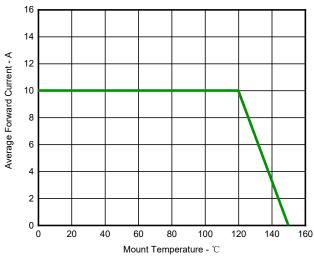
## Electrical characteristics per line@25°C

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VF	-	-	0.48	V	I <sub>F</sub> = 10A
Reverse current	I <sub>R</sub>	-	-	0.5	mA	V <sub>R</sub> = 45V,T <sub>A</sub> = 25℃
Reverse current	I <sub>R</sub>	-	-	50	mA	V <sub>R</sub> = 45V,T <sub>A</sub> = 125℃

# Absolute maximum rating@25℃

Parameter	Symbol	limits	Unit
Reverse voltage (DC)	$V_{RM}$	45	V
Average rectified forward current	lF	10	А
Peak Forward Surge Current 8.3ms Single Half Sine Wave Superimposed on Rated Load	I <sub>FSM</sub>	175	А
Thermal resistance from junction to Mount	R <sub>0JM</sub>	4	°C/W
Junction and Storage Temperature Range	$T_{J_{i}}T_{STG}$	-55~+150	℃

# **Typical Characteristics**



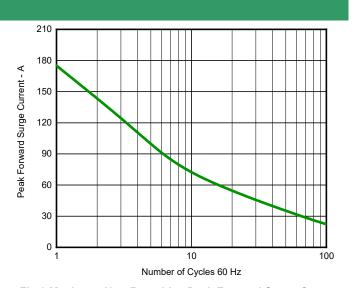


Fig.1 Forward Current Derating Curve

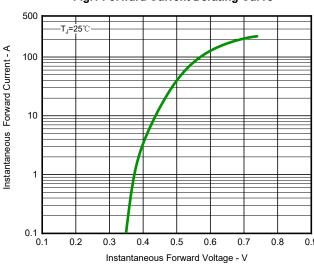


Fig.2 Maximum Non-Repetitive Peak Forward Surge Current

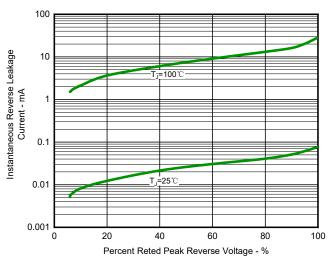
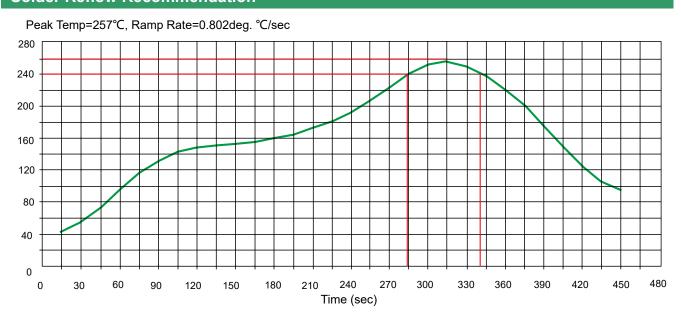


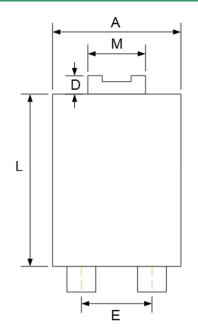
Fig.3 Typical Forward Voltage Characteristics

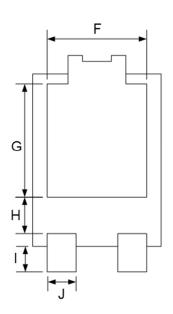
Fig.4 Typical Reverse Leakage Characteristics

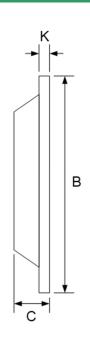
## **Solder Reflow Recommendation**



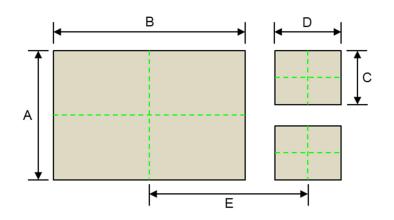
# Product dimension (TO-277B)







Dim	Millim	neters	Inches		
Dim	Min	Max	Min	Max	
А	3.80	4.20	0.150	0.165	
В	6.40	6.60	0.252	0.260	
С	1.00	1.20	0.039	0.047	
D	0.45	0.70	0.018	0.028	
Е	1.65	1.95	0.065	0.076	
F	2.90	3.30	0.114	0.130	
G	3.25	3.85	0.128	0.151	
Н	1.00	1.40	0.039	0.055	
1	0.45	0.70	0.018	0.028	
J	0.80	1.00	0.031	0.039	
К	0.20	0.45	0.008	0.018	
L	5.30	5.50	0.209	0.216	
М	1.70	1.90	0.067	0.075	



Dim	Millimeters	Inches
Α	3.60	0.142
В	5.35	0.211
С	1.50	0.059
D	1.85	0.073
E	4.30	0.169

Suggested PCB Layout

# Ordering information

Device	Package	Shipping
PSBD3TD45V10H	TO-277B(Pb-free)	5000 / Tape & Reel

# Marking information



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