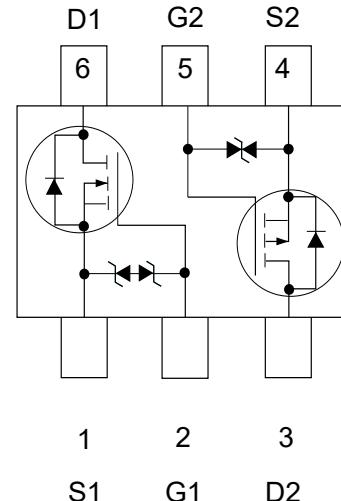


Description

The enhancement mode MOS is extremely high density cell and low on-resistance.

MOSFET Product Summary		
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)
N-Channel 20	0.2@ V _{GS} =4.5V	0.8
	0.25@ V _{GS} =2.5V	
	0.31@ V _{GS} =1.8V	
P-Channel -20	0.45@ V _{GS} =-4.5V	-0.8
	0.62@ V _{GS} =-2.5V	
	0.86@ V _{GS} =-1.8V	



Thermal resistance ratings

Parameter	Symbol	Typical	Maximum	Units
Thermal Resistance-Junction to Ambient,Note:a	R _{θJA}	340	430	°C/W
Thermal Resistance-Junction to Ambient,Note:b	R _{θJA}	465	555	°C/W
Thermal Resistance-Junction to Case	R _{θJC}	280	320	°C/W

a:Surface mounted on FR4 Board using 1 square inch pad size,1oz copper

b:Surface mounted on FR4 Board using minimum pad size,1oz copper

N-Channel

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±8	V
Drain Current	Continuous	I _D	A
	Pulsed	I _D	A
Total Power Dissipation	T _A =25°C,Note:a	P _D	mW
	T _A =25°C,Note:b	P _D	mW

Electrical characteristics per line@25°C(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 1\text{mA}, V_{GS} = 0\text{V}$	20	-	-	V
Zero Gate Voltage Drain Current	I_{DSs}	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}$	-	-	1	μA
Gate-Body Leakage Current	I_{GSs}	$V_{DS} = 0\text{V}, V_{GS} = \pm 8\text{V}$	-	-	± 10	μA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	0.4	0.65	0.8	V
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 4.5\text{V}, I_D = 0.65\text{A}$	-	200	250	$\text{m}\Omega$
		$V_{GS} = 2.5\text{V}, I_D = 0.45\text{A}$	-	250	300	$\text{m}\Omega$
		$V_{GS} = 1.8\text{V}, I_D = 0.25\text{A}$	-	310	450	$\text{m}\Omega$
Forward transfer admittance	g_{FS}	$V_{DS} = 10\text{V}, I_D = 300\text{mA}$	-	1.6	-	s
Input Capacitance	C_{iss}	$V_{GS} = 0\text{V}, V_{DS} = 10\text{V}, f = 1\text{MHz}$	-	48	-	pF
Output Capacitance	C_{oss}		-	13	-	pF
Reverse Transfer Capacitance	C_{rss}		-	10	-	pF
Total Gate Charge	Q_G	$V_{GS} = 4.5\text{V}, V_{DS} = 10\text{V}, I_D = 0.01\text{A}$	-	1.0	-	nC
Gate-Source Charge	Q_{GS}		-	0.1	-	nC
Gate-Drain Charge	Q_{GD}		-	0.42	-	nC
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{V}, V_{GS} = 4.0\text{V}, R_G = 10\Omega, R_L = 67\Omega, I_D = 150\text{mA}$	-	12	-	ns
Turn-Off Delay Time	$t_{d(off)}$		-	50	-	ns
Turn-On Rise Time	t_r		-	13	-	ns
Turn-On Fall Time	t_f		-	25	-	ns
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{V}, I_S = 100\text{mA}$	-	0.7	1	V

P-Channel

Absolute maximum rating@25°C

Rating	Symbol	Value	Units
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 8	V
Drain Current	Continuous	I_D	A
	Pulsed	I_D	A
Total Power Dissipation	$T_A = 25^\circ\text{C}$, Note:a	P_D	mW
	$T_A = 25^\circ\text{C}$, Note:b	P_D	mW

Electrical characteristics per line@25°C(unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	BVDSS	ID =-250uA,VGS=0V	-20	-	-	V
Zero Gate Voltage Drain Current	IDS	VDS =-16V,VGS=0V	-	-	-1	μA
Gate-to-source Leakage Current	IGSS	VDS =0V,VGS=±8V	-	-	±10	μA
ON CHARACTERISTICS						
Gate Threshold Voltage	VGS(th)	VGS = VDS, ID = -250uA	-0.45	-0.55	-0.85	V
Drain-to-source On-resistance (Note 5)	RDS(ON)	VGS=-4.5V, ID =-700mA	-	450	700	mΩ
		VGS=-2.5V, ID =-300mA	-	620	850	mΩ
		VGS=-1.8V, ID =-250mA	-	860	1200	mΩ
Forward Transconductance	gFS	VDS=-5V, ID =-450mA	-	1.25	-	s
CHARGES, CAPACITANCES AND GATE RESISTANCE						
Input Capacitance	Ciss	VGS=0V, VDS =-10V, f=1MHz	-	72	-	pF
Output Capacitance	Coss		-	9.5	-	pF
Reverse Transfer Capacitance	CRSS		-	9.8	-	pF
Total Gate Charge	QG(TOT)	VGS =-4.5V,VDS=-10V, ID =-450mA	-	0.9	-	nC
Threshold Gate Charge	QG(TH)		-	0.1	-	nC
Gate-to-Source Charge	QGS		-	0.15	-	nC
Gate-to-Drain Charge	QGD		-	0.3	-	nC
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	td(ON)	VGS =-4.5V, VDS=-10V , ID =-450mA RG=6Ω	-	43	-	nS
Rise Time	tr		-	137	-	nS
Turn-Off Delay Time	td(OFF)		-	1450	-	nS
Fall Time	tf		-	2050	-	nS
BODY DIODE CHARACTERISTICS						
Forward Voltage	VSD	VGS=0V,IS=-150mA	-0.5	-0.65	-1.1	V

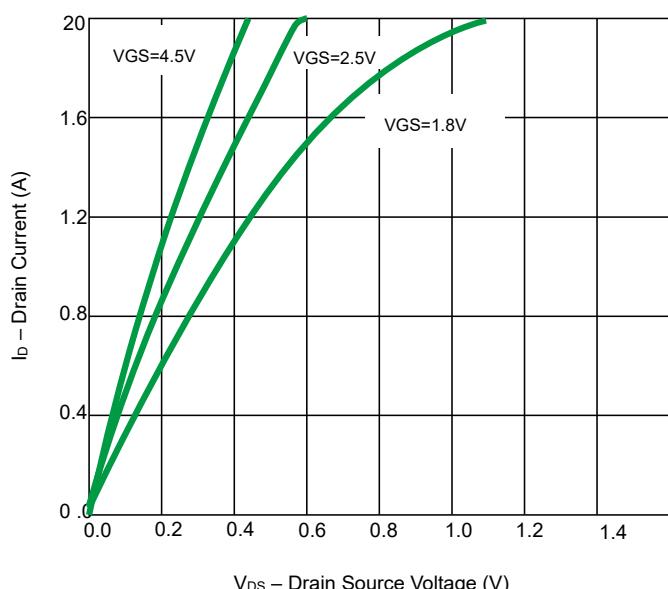
N-Channel**Typical Characteristics**V_{DS} – Drain Source Voltage (V)

Fig 1. Output Characteristics

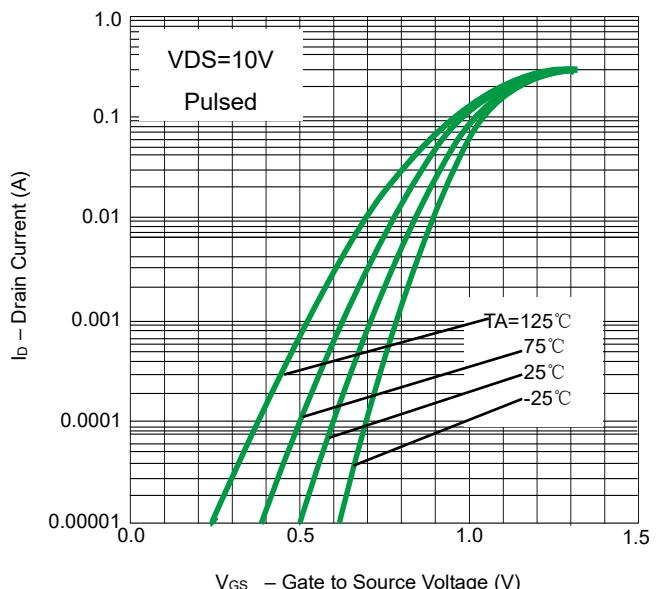
V_{GS} – Gate to Source Voltage (V)

Fig 2. Transfer Characteristics

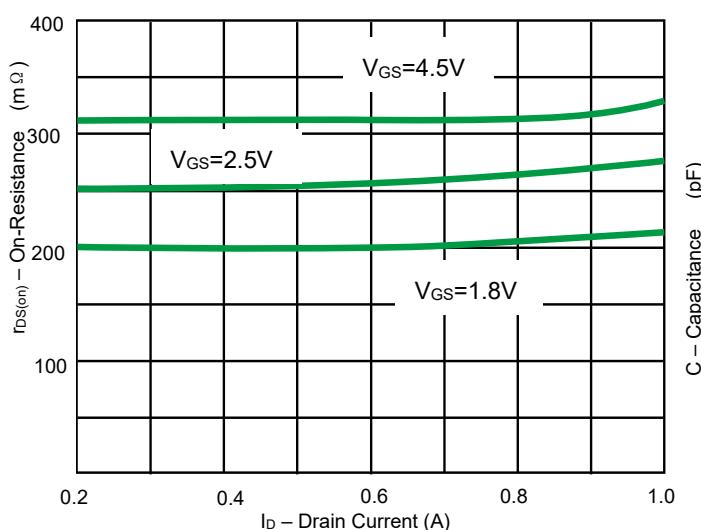


Fig 3. On-Resistance vs. Drain Current

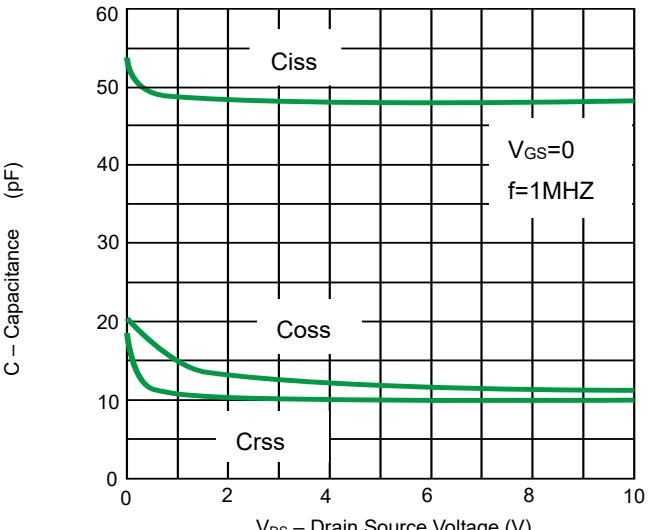


Fig 4. Capacitance

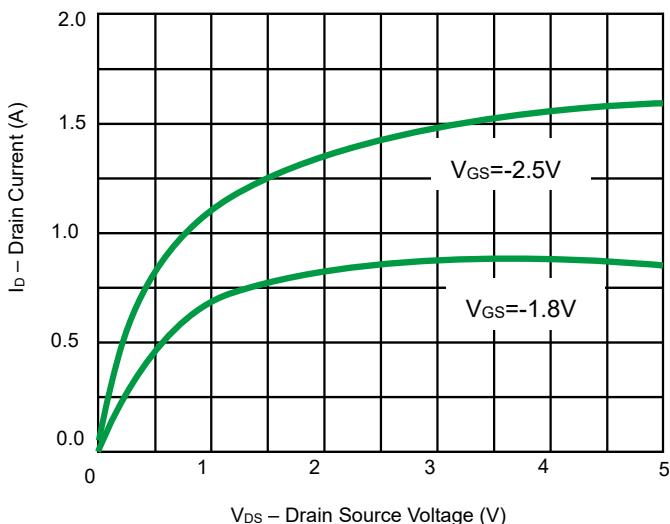
P-Channel**Typical Characteristics**

Fig 1. Output Characteristics

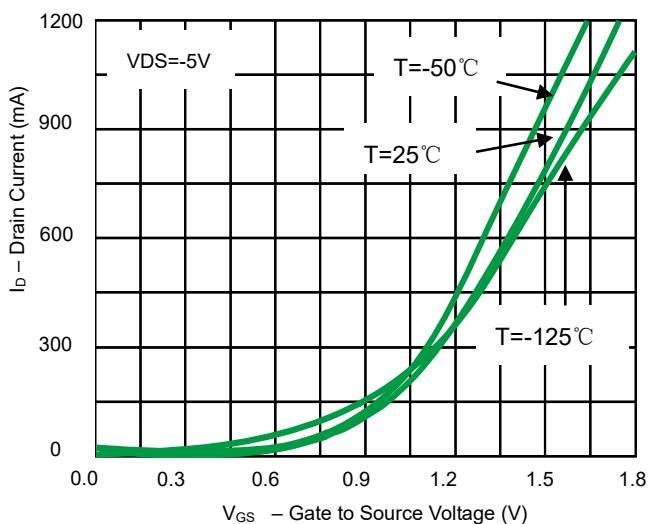


Fig 2. Transfer Characteristics

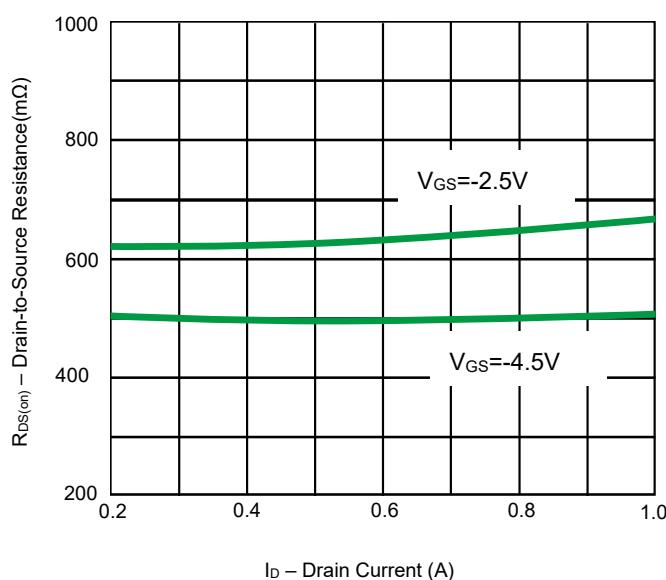


Fig 3. On-Resistance vs. Drain Current

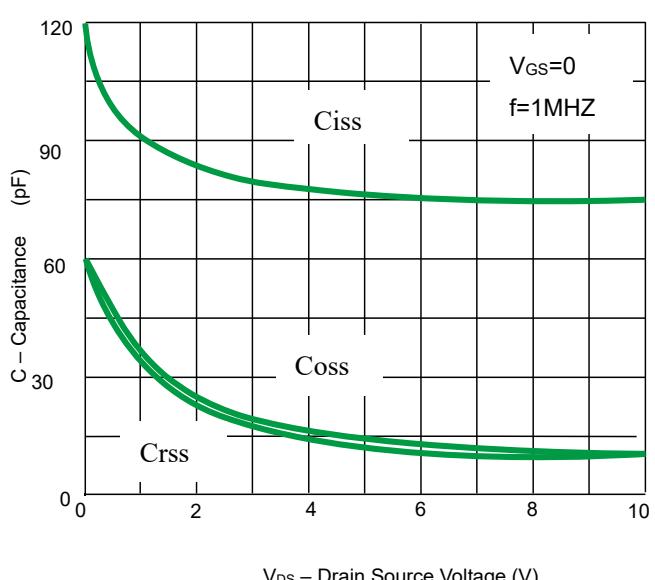
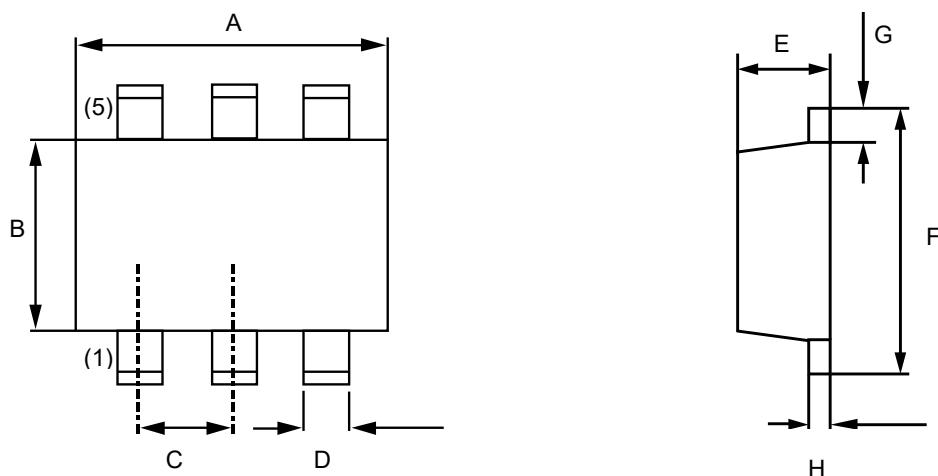
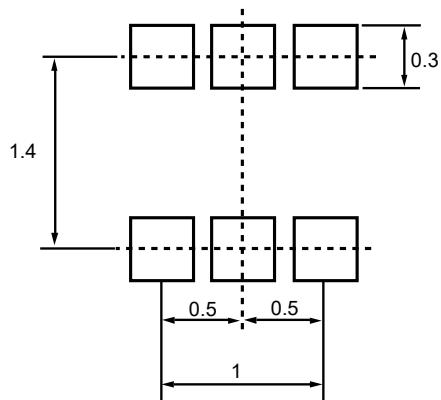


Fig 4. Capacitance

Product dimension (SOT-563)

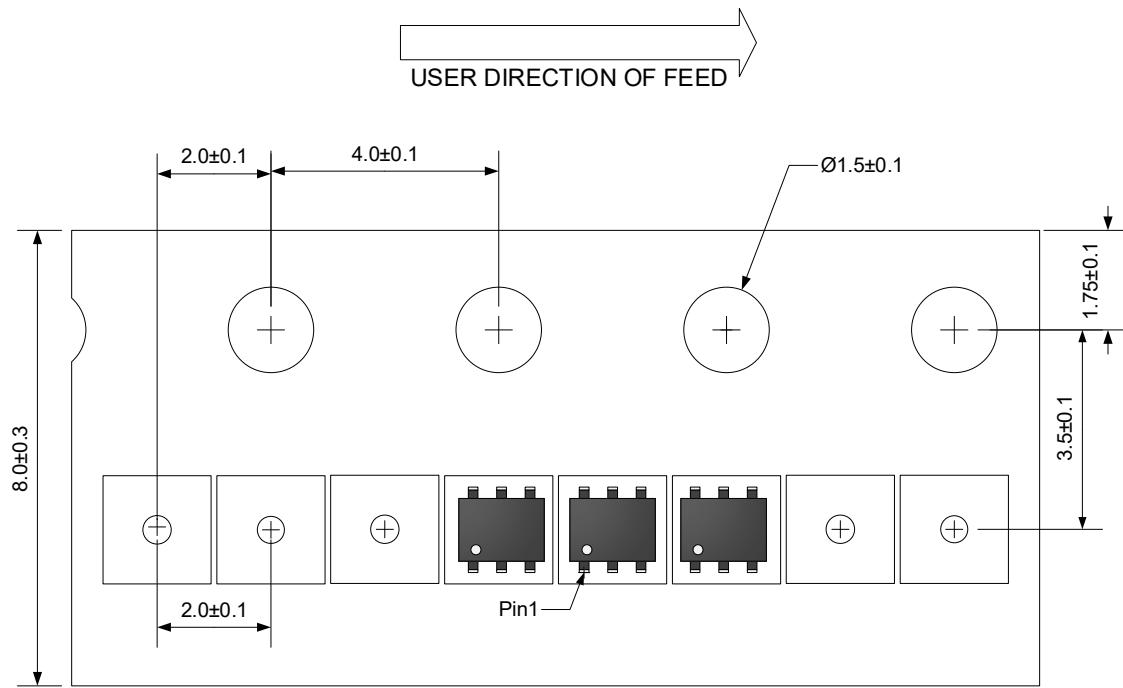


Dim	Millimeters		Inches	
	MIN	MAX	MIN	MAX
A	1.50	1.70	0.059	0.067
B	1.10	1.30	0.043	0.051
C	0.50BSC		0.020BSC	
D	0.17	0.27	0.007	0.011
E	0.50	0.60	0.020	0.024
F	1.50	1.70	0.059	0.067
G	0.10	0.30	0.004	0.012
H	0.08	0.16	0.003	0.006



Ordering information

Device	Package	Shipping
PDM6ET20V08E	SOT-563 (Pb-Free)	8000 / Tape & Reel

Load with information

Unit:mm

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